

What is claimed is:

1. A RAM-incorporated driver which drives a display section, based on still-image data and moving-image data, the RAM-incorporated driver comprising:

first and second bus lines for transferring the still-image data and the moving-image data, respectively;

a RAM which stores the still-image data and the moving-image data transferred through the first and second bus lines;

a first control circuit which controls writing or reading with respect to the RAM of the still-image data or the moving-image data that has been transferred separately over the corresponding first or second bus line, based on a given command; and

a second control circuit that is independent of the first control circuit, which controls the reading of the still-image data or the moving-image data stored in the RAM, and driving the display section.

2. The RAM-incorporated driver as defined by claim 1;

wherein the RAM comprises a first port in which the writing of the still-image data is executed via the first bus line, a second port in which the writing of the moving-image data is executed via the second bus line, and a memory cell having a third port for the reading out the display data.

3. A RAM-incorporated driver which drives a display section, based on a command from an external MPU, and still-image data and moving-image data, the RAM-incorporated driver comprising:

5 a first bus line which transfers the still-image data from the external MPU;

a second bus line which transfers the moving-image data from the external MPU;

10 a RAM which stores the still-image data and the moving-image data;

a first column address control circuit which specifies a column address in the RAM for writing the still-image data;

15 a second column address control circuit which specifies a column address in the RAM for writing the moving-image data;

a first page address control circuit which specifies a page address in the RAM for writing the still-image data;

a second page address control circuit which specifies a page address in the RAM for writing the moving-image data;

20 an MPU-related control circuit which controls the first and second column address control circuits and the first and second page address control circuits, based on the command from the external MPU;

25 a display address control circuit which controls reading of the still-image data and the moving-image data stored in the RAM, as display data; and

a driver-related control circuit which controls the display address control circuit independently of the MPU-related control circuit.

5 4. The RAM-incorporated driver as defined by claim 3; comprising:

 a first pair of bit lines connected to a memory cell of the RAM;

 a second pair of bit lines connected to a memory cell of the RAM;

 a first column switch controlled by the first column address control circuit and connecting the first pair of bit lines with the first bus line;

 a second column switch controlled by the second column address control circuit and connecting the second pair of bit lines with the second bus line;

 a first word line which transfers a signal from the first page address control circuit to a control terminal of a first switch provided between a memory element within the memory cell and the first pair of bit lines; and

 a second word line which transfers a signal from the second page address control circuit to a control terminal of a second switch provided between the memory element and the second pair of bit lines.

25 5. The RAM-incorporated driver as defined by claim 4;

wherein the RAM has a first RAM which stores the still-image data that has been transferred via the first bus line and a second RAM which stores the moving-image data that has been transferred via the second bus line,

5 wherein the display address control circuit comprises a first display address control circuit which controls the reading of still-image data from the first RAM as display data and a second display address control circuit which controls the reading of moving-image from the second RAM as
10 display data.

wherein, in the first RAM, write addresses is controlled by the first column address control circuit and the first page address control circuit, and

15 wherein, in the second RAM, write addresses are controlled by the second column address control circuit and the second page address control circuit.

6. The RAM-incorporated driver as defined by claim 3; wherein the first column address control circuit
20 specifies a read column address in the RAM, based on a signal from the MPU-related control circuit, and

wherein the first page address control circuit specifies a read page address in the RAM, based on a signal from the MPU-related control circuit.

25 7. The RAM-incorporated driver as defined by claim 4;

wherein the first column address control circuit specifies a read column address in the RAM, based on a signal from the MPU-related control circuit, and

wherein the first page address control circuit specifies
5 a read page address in the RAM, based on a signal from the MPU-related control circuit.

8. The RAM-incorporated driver as defined by claim 5;

wherein the first column address control circuit
10 specifies a read column address in the RAM, based on a signal from the MPU-related control circuit, and

wherein the first page address control circuit specifies
15 a read page address in the RAM, based on a signal from the MPU-related control circuit.

9. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

20 the RAM-incorporated driver, as defined by claim 1, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

25 10. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 2,
5 which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

11. A display unit comprising:

10 a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 3,
which drives the plurality of first electrodes; and

15 a scanning driver which scans and drives the plurality of second electrodes.

12. A display unit comprising:

20 a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 4,
which drives the plurality of first electrodes; and

25 a scanning driver which scans and drives the plurality of second electrodes.

13. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 5,
5 which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

14. A display unit comprising:

10 a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 6,
which drives the plurality of first electrodes; and

15 a scanning driver which scans and drives the plurality of second electrodes.

15. A display unit comprising:

20 a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 7,
which drives the plurality of first electrodes; and

25 a scanning driver which scans and drives the plurality of second electrodes.

16. A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 8,
5 which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

17. Electronic equipment comprising:

the display unit as defined by claim 9; and

an MPU which supplies the command, the still-image data, and the moving-image data to the display unit.

18. Electronic equipment which drives a display section to display an image, based on still-image data and moving-image data, the electronic equipment comprising:

the RAM-incorporated driver as defined by claim 1;

means for setting a still-image area, with respect to a display area of the display section in which an image is displayed based on image data stored in a RAM of the RAM-incorporated driver;

means for writing the still-image data in a storage area of the RAM, the storage area corresponding to the still-image area;

means for setting a moving-image area with respect to the display area; and

means for writing the moving-image data into a storage area of the RAM, the storage area corresponding to the moving-image area.

5 19. Electronic equipment which drives a display section to display an image, based on still-image data and moving-image data, the electronic equipment comprising:

the RAM-incorporated driver as defined by claim 1;

10 means for setting a still-image area, with respect to a display area of the display section in which an image is displayed based on image data stored in a RAM of the RAM-incorporated driver:

15 means for writing the still-image data in a storage area of the RAM, the storage are corresponding to the still-image area;

means for setting an arbitrary moving-image area with respect to the still-image area; and

20 means for writing the moving-image data into a storage area of the RAM, the storage area corresponding to the moving-image area.